



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

Vision and Mission of the University

VISION

The University is primarily promoting quality of education in the areas of Science, Technology, Engineering and Mathematics (STEM) as four academic pillars of education, to excel in teaching, learning, research, consultancy and placements through innovative practices with global perspective.

MISSION

Design an Industry relevant curriculum from time to time with a Global perspective Promoting quality education by embracing ICT delivery mechanism with continuous pedagogy through e-learning mechanism Spread across for industry collaborations with a focus to pre-training and placements for technology transfer to society Establishing centers of excellence to promote research and innovations in multidisciplinary areas to bring in patent culture and consultancy practices International Collaborations for student outreach Facilitating international students to study in JNTUK to infuse cross culture learning practices.

Vision and Mission of the Institute

Vision and Mission of the Department



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester

S. No.	Course Title	L	T	P	C
1	Mathematical Foundations for Communication Engineering	3	1	0	4
2	Advanced Digital Communications	3	1	0	4
3	ARM Controllers and Embedded 'C'	3	1	0	4
4	1. FPGA based System Design 2. Detection and Estimation Theory 3. Cognitive Radio	3	0	0	3
5	1. Software Defined Radio 2. Mobile Cellular Communications (Including 5G & B5G) 3. Radar Systems	3	0	0	3
6	Advanced Digital Communications Lab	0	1	2	2
7	Embedded System Design Lab	0	1	2	2
8	Seminar-I	0	0	2	1
	TOTAL	15	5	6	23

List of Professional Elective Courses in I Semester (Electives– I & II)

S.No.	CourseTitle
1	FPGA based System Design
2	Detection and Estimation Theory
3	Cognitive Radio
4	Software Defined Radio
5	Mobile Cellular Communications (Including 5G & B5G)
6	Radar Systems

@Minimum2/3themesper elective



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester

S. No.	Course Title	L	T	P	C
1	System Design with RTOS & Embedded LINUX	3	1	0	4
2	Advanced Signal and Image Processing	3	1	0	4
3	Information Theory and Coding	3	1	0	4
4	1. Advanced C++ Programming 2. IoT & its Communication Protocols 3. AI in Signal Processing and Wireless Communication	3	0	0	3
5	1. Mobile Networks 2. Radar Signal Processing 3. DSP Processors & Architectures	3	0	0	3
6	Wireless Communications Lab	0	1	2	2
7	System Design with RTOS & Embedded Linux Lab	0	1	2	2
8	Seminar–II	0	0	2	1
	TOTAL	15	5	6	23

List of Professional Elective Courses in II Semester (Electives III & IV)

S.No.	Course Title
1	Advanced C++ Programming
2	IoT & its Communication Protocols
3	AI in Signal Processing and Wireless Communication
4	Mobile Networks
5	Radar Signal Processing
6	DSP Processors & Architectures

@Minimum 2/3 themes per elective



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

III Semester

S. No	Course Title	L	T	P	C
1	Research Methodology and IPR / Swayam12weekMOOCcourse– RM&IPR	3	0	0	3
2	SummerInternship/Industrial Training (8-10 weeks)*	-	-	-	3
3	ComprehensiveViva [#]	-	-	-	2
4	DissertationPart– A ^{\$}	-	-	20	10
TOTAL		3	-	20	18

*Student attended during summer/ year break and assessment will be done in 3rdSem.

Comprehensive viva can be conducted courses completed upto second sem.

\$ Dissertation – PartA, internal assessment

IV Semester

S. No.	Course Title	L	T	P	C
1	Dissertation Part– B [%]	-	-	32	16
TOTAL		-	-	32	16

%External Assessment



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	MATHEMATICAL FOUNDATIONS FOR COMMUNICATION ENGINEERING	L	T	P	C
		3	1	0	4

COURSE OBJECTIVES:

1. To introduce foundational concepts in probability, statistics, and hypothesis testing.
2. To develop understanding of stochastic processes and their applications.
3. To equip students with numerical methods for solving mathematical problems.
4. To explore optimization techniques for constrained and unconstrained problems.
5. To familiarize students with wavelet transforms and multi-resolution analysis.

Unit – I: Probability and Statistics:

Sampling distributions, Estimation of parameters (point estimation – unbiasedness & minimum variance, basics of interval estimation – confidence interval for mean), Testing of hypotheses (one and two sample tests for mean), Linear regression, Introduction to non-linear regression.

Unit II: Stochastic process:

Random processes, Random walk, Markov process with special emphasis on Markov chain

Unit – III: Numerical Analysis:

Introduction to Interpolation formulae [Bessel's & Sterling's], Roots of transcendental equations [Bisection, Regula-Falsi & Newton-Raphson] Solutions of simultaneous non-linear equations [Newton's method], Numerical solution of Ordinary Differential equation [Modified Euler's method, fourth order Runge-Kutta method], Matrix Eigen value and Eigen vector problems.

Unit IV: Optimization Technique:

Calculus of several variables, Implicit function theorem, Nature of singular points, Necessary and sufficient conditions for optimization, Constrained Optimization, Lagrange multipliers, Gradient method – steepest descent method.

Unit V: Wavelet Transform:

Resolution problems, Multi-resolution analysis, Continuous & discrete wavelet transform

TEXT BOOKS:

1. A. Papoulis and S. Unnikrishnan Pillai, "Probability, Random Variables and Stochastic Processes," Fourth Edition, McGraw Hill. (Indian Edition is available).
2. Gilbert Strang, "Linear Algebra and its applications", Thomson Learning Inc, 4th Edition.

REFERENCE BOOKS:

1. H.Stark and J. Woods, 'Probability and Random Processes with Applications to Signal Processing,' Third Edition, Pearson Education. (Indian Edition is available).
2. Steven M. Kay, "Intuitive Probability and Random Process using MATLAB", Springer Publications.
3. Todd K Moon, Wynn C. Stirling" Mathematical Methods and Algorithms for Signal Processing, Prentice Hall.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Apply statistical methods for estimation and hypothesis testing.
2. Analyze and model stochastic processes including Markov chains.
3. Solve equations and differential problems using numerical techniques.
4. Optimize multivariable functions using analytical and numerical methods.
5. Implement wavelet transforms for signal and data analysis.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	ADVANCED DIGITAL COMMUNICATIONS	L	T	P	C
		3	1	0	4

COURSE OBJECTIVES

1. To learn the components and models of digital communication systems.
2. To explore signal representation and various digital modulation techniques.
3. To analyse receiver performance in additive white Gaussian noise (AWGN) environments.
4. To study estimation techniques and design principles for band-limited channels.
5. To examine advanced receiver architectures, equalization methods, and multicarrier systems like OFDM.

UNIT I: Elements and Channels in Digital Communication Elements of a Digital Communication System, Mathematical Models for Communication Channels, Communication Channels and Their Characteristics

UNIT II: Signal Representation and Modulation Techniques Representation of Bandpass Signals and Systems, Signal Space Representations, Representation of Digitally Modulated Signals, Memoryless Modulation Methods: Pulse Amplitude Modulation, Phase Modulation Schemes, Quadrature Amplitude Modulation, Multi-dimensional Signaling, Spectral Characteristics of Digitally Modulated Signals

UNIT III: Optimum Receivers and Performance in AWGN Optimum Receiver for Signals Corrupted by AWGN, Performance of the Optimum Receiver for Memoryless Modulation, Optimum Receiver for CPM Signals and Signals with Random Phase in AWGN Channel

UNIT IV: Estimation and Band-limited Channel Design Signal Parameter Estimation, Carrier Phase Estimation, Symbol Timing Estimation, Joint Estimation of Carrier Phase and Symbol Timing, Performance Characteristics of ML Estimators, Characterization of Band-limited Channels, Signal Design for Band-limited Channels, Probability of Error in Detection of PAM, Modulation Codes for Spectrum Shaping

UNIT V: Advanced Receivers, Equalization, and Multicarrier Systems Optimum Receiver for Channels with ISI and AWGN, Linear Equalization, Decision Feedback Equalization, Reduced Complexity ML Detectors, Iterative Equalization and Decoding (Turbo Equalization), Multicarrier Systems: Multi Carrier Communications, Orthogonal Frequency Division Multiplexing (OFDM), Modulation and Demodulation of OFDM System, Algorithm Implementation IFFT/FFT of OFDM, Peak to Average Power Ratio in Multi Carrier Modulation



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	ARM CONTROLLERS AND EMBEDDED 'C'	L	T	P	C
		3	1	0	4

COURSE OBJECTIVES

1. To introduce ARM processor architecture, instruction sets, and efficient programming techniques.
2. To explore exception handling, memory hierarchy, and management units in ARM systems.
3. To develop embedded system applications using ARM Cortex-M microcontrollers.
4. To implement peripheral interfacing techniques including UART, ADC/DAC, and GPIO.
5. To learn communication protocols like I²C and SPI through practical case studies.

UNIT- I: ARM Processor Fundamentals: ARM Design Philosophy, Registers, CPSR, Pipeline, Exceptions, Interrupts and Vector Table, Core Extensions.

Introduction to the ARM Instruction Set: Data Processing Instructions, Branch Instructions, Load Store Instructions, Software Interrupt Instruction, PSR Instructions.

Introduction to the Thumb Instruction Set: Thumb Register Usage, Branch Instructions, Data Processing Instructions, Load-Store Instructions, Stack instructions, Software Interrupt Instruction.

Efficient C Programming: Basic C Data Types, C Looping Structures, Register Allocation, Function Calls, Structure Arrangement.

Writing and Optimizing ARM Assembly Code: Writing Assembly Code, Profiling and Cycle Counting, Instruction Scheduling, Register Allocation, Conditional Execution, Looping Constructs.

UNIT- II:

Exception and Interrupt Handling: Exception Handling, Interrupts, Interrupt Handling schemes
Caches: The Memory Hierarchy and Cache Memory, Cache Architecture, Cache Policy, Flushing and Cleaning Cache Memory.

Memory Protection Units: Protected Regions, Initializing the MPU, Caches and Write Buffer.

Memory Management Units: Moving from an MPU to an MMU, How Virtual Memory Works, Details of the ARM MMU, Page Tables, Translation Lookaside Buffer, Domains And Memory Access Permission, The Fast Context Switch Extension

UNIT- III:

Introduction: Definition of Embedded Systems, Real life examples of embedded systems, Basics of Developing for Embedded Systems



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

ARM Instruction set Architecture: ARM Cortex-M Organization, Arithmetic, Logical and Shift instructions, Data Movement Instructions, Branch instructions, Program Status register, Bitwise logic operations, Sign and Zero extension, Data Comparison, Memory addressing, Branch and conditional execution, Control structures, Subroutines, 64-bit data processing.

GPIO: GPIO Input Modes, GPIO Output Modes, Memory-mapped I/O, Push button, Programming exercises on GPIO and Push-button

General-purpose Timers: Clock Configuration, Timer Organization, and Counting Modes, Timer Update Events, PWM Registers, Configuration and initialization of PWM block, Programming exercises on the selection of clock source, Timer's concept, and PWM

UNIT-IV:

UART: UART Block, UART Registers, UART baud rate calculation, Configuration and initialization of UART

ADC/DAC: ADC & DAC registers, pin configuration, ADC modes, Configuring ADC and DAC module, Programming exercises on ADC and DAC

Interfacing: Keypad, LCD, and Seven segment display interfacing with ARM Cortex-M3 Microcontroller

UNIT-V:

Inter-Integrated Circuit (I²C): I²C operating modes, Configuration of I²C, Interface a sensor using I²C protocol

Serial Peripheral Interface (SPI): SPI Modes, Master operation, Slave operation, Configuration of SPI

Case Study: Smart Home-Smart Door Locks and Interface a temperature sensor with an I²C Module to measure the room temperature.

TEXT BOOKS:

1. A.Sloss, D.Symes, C.Wright, "ARM system Developers Guide: Designing and Optimizing System Software", Morgan Kaufmann publishers, 2012.
2. Dr.Yifeng Zhu "Embedded Systems with ARM Cortex-M Microcontrollers in Assembly and C" Third edition, 2018

REFERENCE BOOKS:

1. Steve Furber, "ARM System on Chip Architecture", 2nd ed., Addison Wesley Professional,2000.
2. Valvano, J,"Embedded microcomputer systems: real time interfacing", 3rd Edition, Cengage Learning, 2011.
3. Frank Vahid, TonyGivargis, "Embedded System Design", J Wiley India,2005.
4. Ariel Lutenberg, Pablo Gomez, Eric Pernia "A Beginner's Guide to Designing Embedded System Applications on Arm Cortex-M Microcontrollers"
5. Qing Li, Caroline Yao "Real-time concepts for Embedded Systems" CMP books.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Demonstrate proficiency in ARM instruction sets and assembly code optimization.
2. Configure and manage exceptions, interrupts, and memory systems in ARM-based designs
3. Develop embedded applications using GPIO, timers, and control structures.
4. Interface and program peripherals such as UART, ADC/DAC, and display modules.
5. Apply I²C and SPI protocols in real-world scenarios like smart home systems



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	FPGA BASED SYSTEM DESIGN (ELECTIVE-I)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To impart knowledge of fundamental digital logic design including Boolean algebra, combinational circuits, sequential circuits, hazards, and timing analysis for modeling reliable digital systems.
2. To introduce Verilog HDL for describing digital systems using gate-level, dataflow, and behavioral modeling, along with simulation of multiplexers, decoders, adders, registers, and flip-flops.
3. To develop the ability to design and simulate practical digital systems such as adders, multipliers, controllers, state machines, and real-time applications like traffic light controllers and display decoders.
4. To provide understanding of programmable logic devices (SPLDs, CPLDs, FPGAs) and techniques for implementing functions, optimizing resources, analyzing trade-offs in cost, performance, and capacity.
5. To expose students to advanced SoC FPGA concepts including Xilinx FPGA/Zynq architectures, interconnect protocols (AMBA, OCP, PCIe, Ethernet, Gen-Z, CCIX), and system-level integration for modern high-performance embedded systems.

UNIT – I:

Combinational Logic, Boolean Algebra and Algebraic Simplification, Karnaugh Maps, Designing with NAND and NOR Gates, Hazards in Combinational Circuits, Flip-Flops and Latches, Mealy Sequential Circuit Design, Design of a Moore Sequential Circuit, Equivalent States and Reduction of State Tables, Sequential Circuit Timing, Tristate Logic and Busses.

UNIT-II:

Basic Concepts, Modules and Ports, Gate Level Modeling, Dataflow Modeling, Behavioral Modeling, Timings and Delays. Modeling multiplexers, decoders, adders, encoders, latches, flip-flops, registers, counters in Verilog HDL.

UNIT -III:

BCD to 7-Segment Display Decoder, A BCD Adder, 32-Bit Adders, Traffic Light Controller, State Graphs for Control Circuits, Scoreboard and Controller, Synchronization and Debouncing, A Shift-and-Add Multiplier, Array Multiplier, A Signed Integer/Fraction Multiplier. State Machine Charts: Derivation of SM Charts, Realization of SM Charts.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

UNIT- IV:

Brief Overview of Programmable Logic Devices, Simple Programmable Logic Devices (SPLDs), Complex Programmable Logic Devices (CPLDs), Field-Programmable Gate Arrays (FPGAs). Designing with Field Programmable Gate Arrays: Implementing Functions in FPGAs, Implementing Functions Using Shannon’s Decomposition, Carry Chains in FPGAs, Cascade Chains in FPGAs, Examples of Logic Blocks in Commercial FPGAs, Dedicated Memory in FPGAs, Dedicated Multipliers in FPGAs, Cost of Programmability, FPGAs and One-Hot State Assignment, FPGA Capacity: Maximum Gates versus Usable Gates.

UNIT-V:

Xilinx FPGA devices overview, Xilinx SoC overview and history, Xilinx Zynq-7000 SoC family hardware features, Xilinx Zynq Ultrascale+ MPSoC family overview, On-chip buses and interconnects overview, ARM AMBA interconnect protocols suite, OCP interconnect protocol, DMA engines and data movements, Legacy off-chip interconnects overview, Introduction to the PCIe interconnect, Ethernet interconnect, Introduction to the Gen-Z protocol, CCIX protocol and off-chip data coherency.

TEXT BOOKS:

1. Charles H. Roth Jr., Lizy Kurian John, Beyeong Kil Lee, Digital Systems Design Using Verilog, CL Engineering, 1st edition, 2015.
2. Samir Palnitkar, Verilog HDL, Pearson Education, 2nd edition, 2004.
3. Charles H. Roth Jr., Fundamentals of Logic Design, CL Engineering, 7th edition, 2013.
4. John F. Wakerly, Digital Design – Principles and Practices, Pearson, 4th edition, 2008.

REFERENCE BOOKS:

1. W. Wolf, FPGA-Based System Design, Pearson, 1st edition, 2004.
2. D. Gajski, S. Abdi, A. Gerstlauer, G. Schirner, Embedded System Design: Modeling, Synthesis, Verification, Springer, 2009.
3. Steve Kilts, Advanced FPGA Design: Architecture, Implementation, and Optimization, Wiley-IEEE Press, 1st edition, 2007.
4. J. Bhaskar, A Verilog HDL Primer, Star Galaxy Publishing, 3rd edition, 2005.

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Apply the principles of Boolean algebra, combinational logic design, sequential circuit design, and timing analysis to model and analyze digital systems.
2. Develop and implement digital system modules in Verilog HDL using gate-level, dataflow, and behavioral modeling approaches.
3. Design and simulate practical digital systems such as adders, multipliers, controllers, and state machines using state graphs and SM charts.
4. Analyze and implement logic functions on programmable logic devices (SPLDs, CPLDs, FPGAs) and evaluate the trade-offs in cost, capacity, and performance.
5. Demonstrate knowledge of SoC FPGAs, Xilinx Zynq families, interconnect protocols, and system-level integration concepts for modern high-performance digital systems.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	DETECTION AND ESTIMATION THEORY (ELECTIVE-I)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES

1. To introduce the fundamentals of random processes and their classifications.
2. To explore detection theory and decision-making under uncertainty.
3. To study optimal filtering techniques including Wiener and Kalman filters.
4. To develop statistical tools for estimation and hypothesis testing.
5. To apply estimation techniques to analyze and model random processes from data.

UNIT –I:

Random Processes: Discrete Linear Models, Markov Sequences and Processes, Point Processes, and Gaussian Processes.

UNIT –II:

Detection Theory: Basic Detection Problem, Maximum A posteriori Decision Rule, Minimum Probability of Error Classifier, Bayes Decision Rule, Multiple-Class Problem (Bayes)-minimum probability error with and without equal a priori probabilities, Neyman-Pearson Classifier, General Calculation of Probability of Error, General Gaussian Problem, Composite Hypotheses.

UNIT –III:

Linear Minimum Mean-Square Error Filtering: Linear Minimum Mean Squared Error Estimators, Nonlinear Minimum Mean Squared Error Estimators. Innovations, Digital Wiener Filters with Stored Data, Real-time Digital Wiener Filters, Kalman Filters.

UNIT –IV:

Statistics: Measurements, Nonparametric Estimators of Probability Distribution and Density Functions, Point Estimators of Parameters, Measures of the Quality of Estimators, Introduction to Interval Estimates, Distribution of Estimators, Tests of Hypotheses, Simple Linear Regression, Multiple Linear Regression.

UNIT –V:

Estimating the Parameters of Random Processes from Data: Tests for Stationarity and Ergodicity, Model-free Estimation, Model-based Estimation of Autocorrelation Functions, Power Spectral Density Functions.

TEXTBOOKS:

1. Steven M. Kay, “Fundamentals of Statistical signal processing, volume-1: Estimation theory”. Prentice Hall 2011.
2. Steven M. Kay, “Fundamentals of Statistical signal processing, volume-2: Detection theory”. Prentice Hall 2011.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

REFERENCE BOOKS:

1. Harry L. Van Trees, “Detection, Estimation, and Modulation Theory, Part I,” John Wiley & Sons, Inc. 2011.
2. A. Papoulis and S. UnniKrishna Pillai, “Probability, Random Variables and stochastic processes, 4e”. The McGraw-Hill 2010.

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Analyze and model various types of random processes including Markov and Gaussian.
2. Apply Bayesian and Neyman-Pearson decision rules for signal detection problems.
3. Design and implement optimal filters for signal estimation in noisy environments.
4. Use statistical methods for parameter estimation and regression analysis.
5. Estimate characteristics of random processes using both model-free and model-based approaches.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	COGNITIVE RADIO (ELECTIVE-I)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To introduce the fundamentals and characteristics of cognitive radio systems.
2. To explore spectrum sensing techniques and measurement methods.
3. To learn cognitive techniques for radio resource optimization.
4. To study the architecture and functional design of cognitive radio systems.
5. To analyze the role of cognitive radio in next generation wireless networks.

UNIT- I: Introduction to Cognitive Radio:

Definition and characteristics of cognitive radio, Cognitive radio vs. traditional radio systems, Cognitive cycle and functions, Dynamic spectrum access and spectrum holes, Regulatory aspects and spectrum policies, Applications and benefits of cognitive radio

UNIT- II: Spectrum Sensing and Measurement:

Spectrum sensing challenges, Energy detection, matched filter detection, cyclostationary feature detection, Cooperative spectrum sensing techniques, Wideband spectrum sensing, Sensing-throughput trade-off, Spectrum measurement and monitoring methods.

UNIT- III: Introduction to Cognitive Radios:

Marking Radio Self -aware, Cognitive techniques – Position Awareness, Environment Awareness in Cognitive Radios, Optimization of Radio Resources, Artificial Intelligence Techniques.

UNIT- IV: Cognitive Radio Architecture:

Cognitive Radio – Functions Components and Design Rules, Cognition Cycle – Orient, Plan Decide and act phases, Interface Hierarchy, Architecture Maps, Building the Cognitive Radio Architecture on Software defined Radio Architecture.

UNIT- V: Next Generation Wireless Networks:

The XG Network Architecture, Spectrum Sensing, Spectrum Management, Spectrum Mobility, Spectrum Sharing, Upper Layer Issues, Cross Layer Design.

TEXT BOOKS:

1. Joseph Mitola III, Software Radio Architecture: Object-Oriented Approaches to Wireless Systems Engineering, Wiley-IEEE Press, 2000.
2. Bruce A. Fette (Ed.), Cognitive Radio Technology, 2nd Edition, Elsevier, 2009.
3. Alexander M. Wyglinski, Maziar Nekovee, Thomas Hou, Cognitive Radio Communications and Networks: Principles and Practice, Academic Press, 2010.
4. Simon Haykin, Cognitive Radio: Brain-Empowered Wireless Communications, IEEE Journal Article, 2005.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

REFERENCE BOOKS:

1. Huseyin Arslan, Cognitive Radio, Software Defined Radio, and Adaptive Wireless Systems, Springer, 2007.
2. Kwang-Cheng Chen, Ramjee Prasad, Cognitive Radio Networks, Wiley, 2009.
3. Markus Dillinger, Kambiz Madani, Nancy Alonistioti, Software Defined Radio: Architectures, Systems and Functions, Wiley, 2003.
4. Timothy J. Brown, Cognitive Radio Networks, Wiley, 2015.
5. Jeffrey H. Reed, Software Radio: A Modern Approach to Radio Engineering, Prentice Hall, 2002.

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Learn the fundamentals of cognitive radio and dynamic spectrum access.
2. Analyse spectrum sensing techniques and spectrum management policies.
3. Design the Wireless Networks Based on the Cognitive Radios.
4. Gives an Understanding of Cognitive Radio Architecture.
5. Learn Concepts Behind Wireless Networks and Next Generation Networks.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	SOFTWARE DEFINED RADIO (ELECTIVE-II)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To introduce the concepts, characteristics, and evolution of software radios.
2. To explore radio frequency implementation issues and RF front-end design.
3. To learn digital signal generation and synthesis techniques.
4. To study multirate signal processing methods and their applications in receivers.
5. To examine data conversion techniques and performance enhancement strategies.

UNIT- I:

Introduction to Software radio concepts: Introduction, need, characteristics, benefits and design principles of Software Radios. Traditional radio implemented in hardware (first generations of 2G cell phones), Software controlled radio (SCR), Software defined radio (SDR), Ideal software radio (ISR), Ultimate software radio (USR)

UNIT -II:

Radio frequency implementation issues: The purpose of RF Front-End, Dynamic range, RF Receiver Front-End Topologies, Enhanced Flexibility of the RF Chain with Software Radios, Importance of Components to Overall performance, Transmitter Architecture and their issues, Noise and Distortion in RF Chain.

UNIT- III:

Digital generation of signals: Introduction, Comparison of Direct Digital Synthesis with Analog Signal Synthesis, Approaches to Direct Digital Synthesis, Analysis of Spurious Signals, Spurious components due to Periodic Jitter.

UNIT – IV:

Multirate Signal Processing: Introduction, Sample Rate Conversion Principles, Polyphase Filters, Digital Filter Banks, Timing Recovery in Digital receivers Using Multirate Digital Filters.

UNIT – V:

A/D & D/A Conversion: Introduction, Parameters of Ideal Data Converters, Parameters of Practical data Converters, Techniques to improve Data Converter performance, JTRS.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	MOBILE CELLULAR COMMUNICATIONS (INCLUDING 5G & B5G) (ELECTIVE-II)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To introduce the fundamentals and evolution of mobile and cellular communication systems.
2. To learn cellular geometry, frequency reuse, and techniques for coverage enhancement.
3. To explore multiple access techniques and evaluate their spectral efficiencies.
4. To study wireless technologies from 2G to 4G, including their features and limitations.
5. To examine the principles, specifications, and innovations in 5G communication systems.

UNIT 1: Introduction to Mobile and Cellular Communication Systems

Introduction to Mobile and Cellular Communication Systems, Generations of wireless mobile systems, Cellular Geometry, Introduction to cellular concept, Principle of Operation of a Cellular Mobile system, Call transfer operation to/from one mobile phone to another mobile, Analog and Digital Cellular Mobile Systems, Multiple Access Schemes, Existing Mobile Communication Technologies.

UNIT 2: Cellular Geometry, Frequency Reuse, Cell Splitting and Sectoring

Introduction, Cellular Geometry, Frequency Reuse, Improving Coverage and Capacity in Cellular Systems, Cell Splitting, Sectoring, Range Extension by the use of Repeaters, Microcell Zone concept, Picocell Zone Concept

UNIT 3: MAC Techniques and Spectral Efficiencies

Multiple Access Techniques for Wireless Communication and Advanced Transceiver Schemes, capacity and spectral Efficiency of FDMA, TDMA and CDMA.

UNIT 4: Wireless Generations Technologies up to 3G and 4G

3G Air interface technologies, 3G spectrum, Internet Speeds of 2G, 2.5G and 3G Technologies, Limitations of 3G, CDMA, CDMA2000, WCDMA, Comparison of WCDMA and CDMA2000. 4G Evolution, Objectives of the projected 4G, Advantages of 4G network technology over 3G, Applications of 4G, 4G Technologies, Smart Antenna Techniques and Limitations of 4G.

UNIT 5: 5G Communications

Introduction, Principle of operation of 5G Technology, Key parameters and Technical Specifications of 5G, Description of 5G technologies, 5G cellular system Error Correction Techniques

TEXT BOOK:

1. Gottapu Sasi bhushana Rao, Raj Kumar Goswami, M.N.V.S.S Kumar
 “Mobile and Cellular Communication (Including 5G & Beyond and Microstrip Antennas)”,
 Paramount Book Distributors, 2025



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	RADAR SYSTEMS (ELECTIVE-II)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To introduce the fundamental principles and operational concepts of radar systems.
2. To learn various radar types and tracking techniques used in modern radar systems.
3. To explore radar signal detection, information extraction, and estimation methods.
4. To study radar signal processing techniques including Doppler and MTI processing.
5. To examine advanced radar applications in navigation, warfare, and high-resolution imaging.

UNIT -I:

Radar fundamentals and operation: Introduction, principles, types of radar, transmitter functions, wave form spectra, receiver functions, signal processing, Radar equation, Radar cross section.

UNIT -II:

Radar Systems: Pulse, CW, FM-CW, MTI, Doppler and multimode techniques, Tracking Radar: Tracking system parameters, Conical Scan, amplitude comparison DTOA and phase interferometry. Range and velocity tracking, tracking accuracy, types of antennas using in radar systems.

UNIT -III:

Detection of Radar Signals and information extraction and estimation: Detection introduction, threshold detection, Signal integration, Binary integrators, CFAR, Theoretical accuracy of radar measurements, ambiguity function and radar waveform design, correlation detection and matched filter receiver.

UNIT – IV:

Radar signal processing: Signal integration, spectrum analysis, windows and resolution, MTI principles and methods, De staggering and processing, Moving Radars and moving clutter, Doppler processing.

UNIT – V:

Radar Applications: Direction finders, instrument landing systems, Radar beacons, Electronic Warfare, ECM and ECCM, high resolution radar, range and Doppler resolution, Pulse compression (analog and digital), Synthetic aperture radar, millimeter wave radars for radio fuse applications.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	ADVANCED DIGITAL COMMUNICATIONS LAB	L	T	P	C
		0	1	2	2

COURSE OBJECTIVES:

1. To introduce simulation techniques for digital modulation and signal reconstruction.
2. To explore bit error rate (BER) performance of modulation schemes in noisy channels.
3. To analyse signal constellations and pulse shaping for efficient data transmission.
4. To design equalizers for mitigating channel effects in baseband communication.
5. To simulate OFDM systems for high-speed data transmission and spectral efficiency

SIMULATION BASED EXPERIMENTS: (MATLAB SIMULATION)

1. Sampling & reconstruction of low pass signals
2. BPSK Modulation & detection
3. BER of BPSK in AWGN channel
4. QPSK generation & detection
5. BER of QPSK in AWGN channel
6. QAM generation & detection
7. 16 QAM constellation diagram
8. Generation of Nyquist-I pulse
9. Designing an equalizer in the context of baseband binary data transmission
10. OFDM generation and detection



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	EMBEDDED SYSTEM DESIGN LAB	L	T	P	C
		0	1	2	2

COURSE OBJECTIVES:

1. To provide hands-on experience with ARM Cortex-M microcontroller programming.
2. To develop embedded applications using GPIO, timers, interrupts, and serial communication.
3. To explore advanced interfacing techniques including ADC, PWM, LCD, and SD card integration.
4. To implement wireless communication using BLE and Wi-Fi for smart system applications.
5. To design and deploy embedded systems capable of real-time monitoring and control.

EXPERIMENTS USING ARM CORTEX-M MICROCONTROLLER

(NUCLEO board -F429ZI):

1. Program to configure and control General Purpose Input / Output (GPIO) port pins.
2. Program to demonstrate Serial communication. Transmission from Kit and reception from PC using Serial Port on IDE environment use debug terminal to trace the program.
3. Program to demonstrate Time delay program using built in Timer / Counter feature on IDE environment.
4. Program to demonstrate a simple interrupt handler and setting up a timer.
5. Program to Displaying a message in a 2-line x 16 Characters LCD display and verify the result in debug terminal.
6. Program to demonstrate ADC interfacing.
7. Generation of PWM Signal with the objective of introducing the practical application of timers and fundamental principles of control theory.
8. To integrate a micro-SD card with the computing system for the purpose of storing event logs conveniently on the SD card.
9. To establish a connection between the two computing systems using Bluetooth Low Energy (BLE), with the objective of monitoring pertinent information from one system and facilitating gate control through the other system.
10. To enhance the smart home system by enabling it to host a web page through Wi-Fi connectivity, thereby allowing users to access information using a smartphone or PC.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

I Semester	SEMINAR-I	L	T	P	C
		0	0	2	1



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	SYSTEM DESIGN WITH RTOS & EMBEDDED LINUX	L	T	P	C
		3	1	0	4

COURSE OBJECTIVES:

1. To introduce the fundamentals of Real-Time Operating Systems (RTOS) and task management.
2. To learn synchronization, communication mechanisms, and I/O subsystems in RTOS.
3. To explore exception handling, interrupt management, and timer services in real-time systems.
4. To provide hands-on knowledge of Linux kernel operations and shell scripting.
5. To study embedded Linux architecture and techniques for application porting and development.

UNIT -I: Introduction to RTOS and Task Management

Introduction to Real-Time Operating Systems (RTOS): Key characteristics, scheduler, kernel objects and services, system calls, static and dynamic libraries, cross tool chains, Task management: Defining tasks, task states, scheduling, task operations, synchronization, communication, concurrency.

UNIT -II: Synchronization, Communication, and I/O Systems

Semaphores: Operations, use cases, Message Queues: Types, operations, use cases (including pipes, event registers, signals, condition variables), I/O Subsystems: I/O concepts, subsystems, Synchronization and Communication: Resource synchronization methods, critical section, design patterns, priority inversion, common design problems (deadlocks, priority inversion).

UNIT- III: Exceptions, Interrupts, and Timer Services

Exceptions and Interrupts: Definitions, applications, spurious interrupts, Timer Services: Real-time clocks, system clocks, programmable interval timers, timer interrupt service routines.

UNIT- IV: Linux Kernel and Shell Scripting

Introduction to Linux Kernels: Linux basics, GNU utilities, distributions, access methods (CLI, graphical terminal emulators), Bash Shell Commands: Navigation, file handling, system monitoring, environment variables, user-defined variables, Shell Scripting: Script creation, control structures (if-else, loops, case commands), output redirection, practical examples, handling signals, background scripts, basic script functions, alternative shells (dash, zsh).

UNIT -V: Embedded Linux Architecture and Application Porting

Embedded Linux Architecture: Kernel architecture, memory manager, scheduler, file system, I/O and networking subsystems, IPC, user space, startup sequence, Board Support Package: Embedded storage (MTD), embedded file system, embedded device drivers (communication between user space and kernel, character/block drivers, interrupt handling, kernel modules), Porting Applications: Real-time Linux, hard real-time programming, building and debugging (bootloaders, kernel, root file system, device tree).



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

TEXT BOOKS:

1. Qing Li, Caroline Yao (2020), “Real-Time Concepts for Embedded Systems”, CMP Books.
2. Chris Simmonds, “Mastering Embedded Linux Programming” - Second Edition, PACKT Publications Limited.

REFERENCE BOOKS:

1. Karim Yaghmour, “Building Embedded Linux Systems”, O'Reilly & Associates.

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Explain the key features of RTOS and manage tasks using scheduling, synchronization, and communication techniques.
2. Apply semaphores, message queues, and I/O subsystem concepts to design robust real-time applications.
3. Handle exceptions, interrupts, and implement timer services for real-time responsiveness.
4. Utilize Linux commands and scripting techniques to automate system tasks and manage embedded environments.
5. Demonstrate understanding of embedded Linux architecture and port applications using kernel modules and device drivers.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	ADVANCED SIGNAL AND IMAGE PROCESSING	L	T	P	C
		3	1	0	4

COURSE OBJECTIVES:

1. To introduce the fundamentals of digital signal processing and ARM Cortex processor architecture.
2. To learn digital image and video formats, sampling, filtering, and transform techniques.
3. To explore image and video enhancement, restoration, and color processing methods.
4. To study segmentation techniques for image and video analysis including motion and semantic segmentation.
5. To examine compression techniques for efficient image and video storage and transmission.

UNIT- I:

Introduction to Digital Signal Processing, Signals in Time and Frequency Domains, Signals and Filtering, Architecture of ARM Cortex M3/M4 Processor, ADC/ DAC Interfacing to ARM Cortex M3/M4 Processor, Digital Signal Processing Algorithms: Filter Design, FIR Digital filter design, Frequency Domain, Fourier Transform: DFT, FFT, Spectral Analysis

Unit -II

Digital Image and Video Fundamentals: Digital image and video fundamentals and formats, 2-D and 3-D sampling and aliasing, 2-D/3-D filtering, image decimation/interpolation, video sampling and interpolation, Basic image processing operations, Image Transforms, Need for image transforms, DFT, DCT, Walsh, Hadamard transform, Haar transform, Wavelet transform

Unit -III

Image and Video Enhancement and Restoration: Histogram, Point processing, filtering, image restoration, algorithms for 2-D motion estimation, change detection, motion-compensated filtering, frame rate conversion, deinterlacing, video resolution enhancement, Image and Video restoration (recovery). Colour image Processing: Colour fundamentals, Colour models, Conversion of colour models, Pseudo colour image processing, Full colour processing

Unit -IV

Image and Video Segmentation: Discontinuity based segmentation- Line detection, edge detection, thresholding, Region based segmentation, Scene Change Detection, Spatiotemporal Change Detection, Motion Segmentation, Simultaneous Motion Estimation and Segmentation Semantic Video Object Segmentation, Morphological image processing.

Unit- V

Image and Video Compression: Lossless image compression including entropy coding, lossy image compression, video compression techniques, and international standards for image and video compression (JPEG, JPEG 2000, MPEG-2/4, H.264, SVC), Video Quality Assessment.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

TEXT BOOKS:

1. Digital Signal Processing Implementation Using the TMS320C6000 DSP Platform, 1st Edition; by: Naim Dahnoun
2. The Definitive Guide to the ARM Cortex-M3, Joseph Yiu, Second Edition, Elsevier Inc. 2010
3. DSP Applications using 'C' and the TMS320C6X DSK, 1st Edition; by: Chassaing
4. Digital Signal Processing: A System Design Approach, 1st Edition; by: David J Defatta J, Lucas Joseph G & Hodkiss William S; John Wiley
5. Digital Signal Processing with Field Programmable Gate Arrays: 2nd Edition, by: U. Meyer – Base, Springer.

REFERENCE BOOKS:

1. Real - Time Digital Signal Processing: Implementations, Applications, and Experiments with the TMS320C55X, Kuo, Sen M, Lee, Bob H, John Wiley & Sons Ltd.
2. Digital Signal Processing – A Practical Guide for Engineers and Scientists, Steven W Smith, Elsevier
3. Digital Signal Processing - A Student Guide, 1st Edition; by: T.J. Terrel and LikKwan Shark; Macmillan Press; Ltd.
4. Sanjit K. Mitra, "Digital Signal Processing Laboratory Using MATLAB" ,McgrawHill College, ISBN-13: 978-0073108582

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Apply DSP concepts and algorithms using ARM Cortex M3/M4 processors for signal analysis.
2. Analyse image and video formats and perform basic processing using various transform techniques.
3. Implement enhancement and restoration algorithms for image and video quality improvement.
4. Perform segmentation using edge detection, thresholding, and motion estimation techniques.
5. Demonstrate understanding of image and video compression methods including lossless and lossy.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	INFORMATION THEORY AND CODING	L	T	P	C
		3	1	0	4

COURSE OBJECTIVES:

1. To introduce the foundational concepts of information theory including entropy, mutual information, and their applications.
2. To study various source coding techniques and understand their efficiency and implementation.
3. To explore communication channel models and analyze their capacity and performance.
4. To learn video and speech coding techniques for compression and multimedia transmission.
5. To examine error control coding methods for reliable data communication and storage.

Unit -I: Introduction

Information Source, Symbols, and Entropy, Mutual information, information Measures for Continuous Random Variable, Joint and Conditional Entropy, Relative Entropy, Applications Based on information Theoretic Approach.

Unit -II: Source coding

Source Coding Theorem, Kraft inequality, Shannon-Fano Codes, Huffman Codes, Run Length Code, Arithmetic Codes, Lempel-Ziv-Welch Algorithm, Universal Source Codes, Prefix Codes, Variable Length Codes, Uniquely Decodable Codes, instantaneous Codes, Shannon's Theorem, Shannon Fano Encoding Algorithm, Shannon's Noiseless Coding Theorem, Shannon's Noisy Coding Theorem.

Unit -III: Communication channel

Channel and its Capacity, Continuous and Gaussian Channels, Discrete Memory-Less Channels, Symmetric Channel, Binary Erasure Channel, Estimation of Channel Capacity, Noiseless Channel, Channel Efficiency, Shannon's Theorem on Channel Capacity, Mimo Channels, Channel Capacity with Feedback.

Unit -IV: Video and speech coding

Video Coding Basics, Quantization, Symbol Encoding, Intraframe Coding, Predictive Coding, Transform Coding, Subband Coding, Vector Quantization, Interframe Coding, Motion Compensated Coding, Image Compression, Jpeg, LZ78 Compression, Dictionary Based Compression, Statistical Modelling, Speech Coding, Psycho-Acoustic Modelling, Time Frequency Mapping Quantization, Variable Length Coding, Multichannel Correlation and Irrelevancy, Long Term Correlation, Pre-Echo Control, Bit Allocation.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	ADVANCED C++ PROGRAMMING (ELECTIVE-III)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES

1. To introduce object-oriented programming principles and contrast them with procedural paradigms.
2. To develop proficiency in C++ syntax, data structures, and memory management.
3. To learn class design, data abstraction, and object lifecycle management.
4. To explore inheritance, polymorphism, and virtual functions for dynamic behavior.
5. To study advanced C++ features including file I/O, exception handling, templates, and concurrency

UNIT – I: Object-Oriented Thinking: Different paradigms for problem solving, need for OOP paradigm, differences between OOP and Procedure oriented programming, Overview of OOP concepts Abstraction, Encapsulation, Inheritance and Polymorphism. C++ Basics: Structure of a C++ program, Data types, Declaration of variables, Expressions, Operators, Operator Precedence, Evaluation of expressions, Type conversions, Pointers, Arrays, Pointers and Arrays, Strings, Structures, References. Flow control statement- if, switch, while, for, do, break, continue, goto statements. Functions - Scope of variables, Parameter passing, Default

arguments, inline functions, Recursive functions, Pointers to functions. Dynamic memory allocation and de-allocation operators-new and delete, Preprocessor directives.

UNIT – II: C++ Classes and Data Abstraction: Class definition, Class structure, Class objects, Class scope, this pointer, Friends to a class, Static class members, Constant member functions, Constructors and Destructors, Dynamic creation and destruction of objects, Data abstraction, ADT and information hiding.

UNIT – III: Inheritance: Defining a class hierarchy, Different forms of inheritance, Defining the Base and Derived classes, Access to the base class members, Base and Derived class construction, Destructors, Virtual base class. Virtual Functions and Polymorphism: Static and Dynamic binding, virtual functions, Dynamic binding through virtual functions, Virtual function call mechanism, Pure virtual functions, Abstract classes, Implications of polymorphic use of classes, Virtual destructors.

UNIT – IV: C++ I/O: I/O using C functions, Stream classes hierarchy, Stream I/O, File streams and String streams, Overloading operators, Error handling during file operations, Formatted I/O. Exception Handling: Benefits of exception handling, Throwing an exception, The try block, Catching an exception, Exception objects, Exception specifications, Stack unwinding, Rethrowing an exception, Catching all exceptions.

Unit – V: Advanced Topics: Standard Template Libraries. Introduction to Smart Pointers, RValue references, Lambda expressions, Concurrency API



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

TEXT BOOKS:

1. “The C++ Programming Language” – Bjarne Stroustrup
2. Programming: Principles and Practice Using C++” – Bjarne Structure

REFERENCE BOOKS:

1. Advanced C++ by GaianAL ankus, Olena Liina, Rakesh Mane, Vivek Nagarajan,
2. Effective C++: 55 Specific Ways to Improve Your Programs and Designs 3rd Edition
by Scott Meyers (Author)
3. Object Oriented Programming in C++, 4e Paperback – 1 January 2008
by Lafora (Author)

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Apply object-oriented principles such as abstraction, encapsulation, inheritance, and polymorphism to solve real-world problems.
2. Develop robust C++ programs using appropriate control structures, functions, and dynamic memory management.
3. Design and implement classes with constructors, destructors, and member functions to achieve data abstraction and modularity.
4. Utilize inheritance and polymorphism to create flexible and maintainable code with runtime binding and virtual functions.
5. Handle file I/O operations and exceptions effectively, and leverage advanced C++ features like STL, lambda expressions, and concurrency for efficient programming.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	IOT & ITS COMMUNICATION PROTOCOLS	L	T	P	C
	(ELECTIVE-III)	3	0	0	3

COURSE OBJECTIVES:

1. To introduce the architecture, standards, and fundamental technologies of IoT systems.
2. To explore the IoT reference architecture and understand various design and deployment views.
3. To study data link and network layer protocols relevant to IoT communication.
4. To learn transport and session layer protocols used in IoT applications.
5. To examine service layer protocols and security mechanisms essential for reliable and secure IoT.

UNIT-I:

INTRODUCTION: IoT architecture outline, standards - IoT Technology Fundamentals- Devices and gateways, Local and wide area networking, Data management, Business processes in IoT, Everything as a Service(Xia's), M2M and IoT Analytics

UNIT-II:

IOT REFERENCE ARCHITECTURE: Introduction, Functional View, Information View, Deployment and Operational View, Other Relevant architectural views. Real-World Design Constraints- Introduction, Technical Designconstraints

UNIT-III:

IoT DATA LINK LAYER & NETWORK LAYER PROTOCOLS: PHY/MAC Layer (3GPP MTC, IEEE 802.11, IEEE 802.15), Wireless HART,Wave,Bluetooth Low Energy, Zigbee Smart Energy, DASH7 - Network Layer-IPv4,IPv6, 6LoWPAN, 6TiSCH,ND, DHCP, ICMP, RPL, CORPL, CARP

UNIT-IV:

IoT TRANSPORT & SESSION LAYER PROTOCOLS: Transport Layer (TCP, MPTCP, UDP, DCCP, SCTP)-(TLS, DTLS) – Session Layer-HTTP, CoAP, XMPP, AMQP, MQTT

UNIT-V:

IoT SERVICE LAYER PROTOCOLS & SECURITY PROTOCOLS: Service Layer - oneM2M, ETSI M2M, OMA, BBF – Security in IoT Protocols – MAC802.15.4 , 6LoWPAN, RPL, Application Layer

TEXT BOOKS:

1. Daniel Minoli, “Building the Internet of Things with IPv6 and MIPv6: The Evolving World of M2M Communications”, ISBN: 978-1-118-47347-4, Willy Publications ,2016
2. Jan Holler, VlasiosTsiatsis, Catherine Mulligan, Stefan Avesand,Stamatis Karnouskos, David Boyle, “From Machine-to-Machine to the Internet ofThings: Introduction to a New Age of Intelligence”, 1st Edition, Academic Press, 2015.

REFERENCE BOOKS:

1. Bernd Scholz-Reiter, Florian Michahelles, “Architecting the Internet of Things”,ISBN 978-3-642 19156-5 e-ISBN 978-3-642-19157-2, Springer, 2016
2. N. Ida, Sensors, Actuators and Their Interfaces, Scitech Publishers, 2014.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Describe the components of IoT architecture and explain the role of devices, gateways, and networking in IoT ecosystems.
2. analyse the IoT reference architecture and apply design principles considering real-world constraints.
3. Evaluate and compare various data link and network layer protocols used in IoT communication.
4. Implement and utilize transport and session layer protocols for efficient data exchange in IoT systems.
5. Apply service layer protocols and security techniques to ensure safe and scalable IoT deployments.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	AI IN SIGNAL PROCESSING AND WIRELESS COMMUNICATION (ELECTIVE-III)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To introduce the fundamentals of AI, ML, and DL with applications in text and image processing.
2. To explore AI-driven solutions for smart agriculture using IoT and sensor technologies.
3. To study the role of AI in wireless communication systems and antenna design.
4. To apply AI techniques in biomedical and industrial signal processing for predictive and classification tasks.
5. To investigate energy-efficient systems and advanced optimization methods using AI and metaheuristics.

UNIT 1: Fundamentals of AI and Applications in Text and Image Processing

Introduction to Artificial Intelligence (AI), Machine Learning (ML), and Deep Learning (DL), Long Short-Term Memory (LSTM) Deep Neural Networks for Sentiment Classification, Natural Language Processing (NLP) Techniques and Tools, Computer Vision Techniques for Plant Pest and Disease Detection, Image Processing and ANN-based Techniques in Agriculture

UNIT 2: AI Applications in Smart Agriculture and IoT

Plant Disease Identification using IoT and Deep Learning Algorithms, Precision Agriculture: Sensor Technologies and Wireless Sensor Networks (WSN), Smart Irrigation and Fertilization Systems, Smart Pest Control and Early Disease Detection, Integration of Biosensors and IoT for Soil Health Monitoring

UNIT 3: AI in Wireless Communication and Antenna Design

Artificial Neural Networks (ANN) in 5G Wireless Communication, Particle Swarm Optimization (PSO) in Antenna Design, Multiband and Broadband Antenna Design using AI Techniques, Hybrid Bio inspired Approaches for Fractal Antenna Design, AI in Direction of Arrival (DOA) Estimation Using LVMFO

UNIT 4: AI Techniques in Biomedical and Industrial Signal Processing

Prediction of Coronary Artery Disease using Logistic Regression, Acoustic Signal Classification in Milling Operations using ANN, Application of ML and DL in Biomedical Signal Processing, Evaluation Strategies for Signal Classification Models



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

UNIT 5: Energy-Efficient Systems and Advanced Optimization

Energy-Efficient WSN Deployment for Railway Track Health Monitoring, Hybrid MAC Protocols and Spatiotemporal Aggregation Techniques, Optimization Techniques in Signal Processing using Metaheuristics, Comparative Study of ANN, PSO, and Hybrid Approaches

TEXT BOOKS:

1. Abhinav Sharma, Arpit Jain, Ashwini Kumar Arya, Mangey Ram (Eds.)
Artificial Intelligence for Signal Processing and Wireless Communication.
2. Machine Learning for Signal Processing, Authors: Max A. Little,
Publisher: Oxford University Press, 2019

REFERENCE BOOKS:

1. Wireless Sensor Networks: Technology, Protocols, and Applications, Authors:
Kazem Sohraby, Daniel Minoli, Taieb Znati, Publisher: Wiley-Interscience.
2. Deep Learning: Ian Goodfellow, Yoshua Bengio, Aaron Courville
Publisher: MIT Press, 2016

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Apply deep learning models such as LSTM and NLP tools for sentiment analysis and agricultural image processing.
2. Design smart agriculture systems using IoT, biosensors, and AI algorithms for disease detection and resource management.
3. Utilize AI techniques like ANN and PSO for efficient wireless communication and antenna design.
4. Implement ML and DL models for biomedical signal analysis and industrial acoustic signal classification.
5. Develop energy-efficient monitoring systems and optimize signal processing tasks using hybrid and metaheuristic approaches.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	MOBILE NETWORKS (ELECTIVE-IV)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To introduce wireless personal, local, and metropolitan area network standards and architectures.
2. To explore wide area wireless technologies including GSM, CDMA, and WCDMA systems.
3. To learn mobility management and IP-based protocols for wireless internet access.
4. To study wideband wireless technologies such as Ultra-Wideband (UWB) and their operational
5. To examine adhoc wireless networks, 4G technologies, and emerging concepts.

UNIT I: Wireless Area Networks and Standards

Overview of WPAN: IEEE 802.15 (Bluetooth, RFID, Zigbee, WBAN), WLAN (IEEE 802.11), WiMAX (IEEE 802.16) protocols and architectures, Bluetooth network architecture, operation, applications, RFID and Zigbee specifications, WLAN physical and MAC layer mechanisms (CSMA/CA, WiFi MAC), wideband access (802.11n/ac), energy efficiency, WiMAX: Broadband Wireless Access, issues and challenges, comparison with IEEE 802.11.

UNIT II: Wireless Wide Area Networks

GSM, 3G, and Evolution, spread spectrum, CDMA (IS 95 to CDMA 2000), WCDMA, system processing gain, types of spread spectrum systems, power control in CDMA, uplink/downlink (BS to MS, MS to BS).

UNIT III: Wireless Internet and Mobility Management

IP for wireless networks, mobile IP, IPv6 advancements, mobility management functions (location management, handoffs, registration), impact of mobility on TCP, types of TCP in wireless, wireless security standards.

UNIT IV: Wideband Wireless Technologies

UWB Radio Communication, fundamentals, major issues, operation, comparisons with other technologies, advantages and disadvantages.

UNIT V: Adhoc Wireless Networks and 4G Technologies

Characteristics of Adhoc networks, MAC protocols (table-driven, on-demand), routing protocols (DSDV, AODV, DSR, Hybrid), 4G technologies, features, challenges, LTE (FDD vs TDD), smart antennas, OFDM-MIMO systems, adaptive modulation, software-defined radio, cognitive radio.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

TEXTBOOKS:

1. Theodore Rappaport —Wireless Communication, Prentice Hall, 2nd Edition.
2. William Stallings —Wireless Communications and Networks, Prentice Hall.
3. Schwartz —Mobile Wireless Communications, Cambridge University Press.
4. Mark and Zhuang —Wireless Communications and Networking, Prentice Hall.
5. Vijay Garg K, “Wireless Communications and Networks”, 2ndEdition, Morgan Kaufmann Publishers (Elsevier), 2007.

REFERENCE BOOKS:

1. Clint Smith and Daniel Collins, “3G Wireless Networks”, 2nd Edition, Tata McGraw Hill, 2007.
2. Amitabha Ghosh and Rameepat Ratasuk, “Essentials of LTE and LTE-A,” Cambridge University Press, 2011.
3. Dharma Prakash Agrawal and Qing-An Zeng, "Introduction to wireless mobile systems" Thomson India, 2007.
4. Siva Ram Murthy C and Manoj B S, “Ad Hoc Wireless Networks: Architectures and Protocols”, Prentice Hall, 2004.
5. Jonathan Rodriguez, “Fundamentals of 5G Mobile Networks”, John Wiley & Sons.

COURSE OUTCOMES :

After completion of the course, students will be able to:

1. Analyse and compare WPAN, WLAN, and WiMAX standards including their architectures, protocols, and energy efficiency.
2. Explain the evolution of wireless wide area networks and apply concepts of spread spectrum and power control in CDMA systems.
3. Evaluate mobility management techniques and assess the impact of mobility on IP and TCP protocols in wireless environments.
4. Demonstrate understanding of UWB communication systems and compare them with other wideband technologies.
5. Design and assess adhoc network protocols and understand the features and challenges of 4G technologies including LTE, OFDM-MIMO, and cognitive radio.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	RADAR SIGNAL PROCESSING (ELECTIVE-IV)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To introduce the fundamentals of radar systems, radar equations, and matched filter concepts.
2. To study radar signal detection techniques in noisy environments and understand CFAR receivers.
3. To explore waveform selection principles and design optimal radar waveforms for cluttered.
4. To learn pulse compression techniques and their implementation in radar systems.
5. To examine phase coding methods and Doppler-tolerant waveforms for advanced radar .

UNIT-I: Introduction: Radar Block Diagram, Biostatic Radar, Monostatic Radar, Radar Equation, Information Available from Radar Echo. Review of Radar Range Performance– General Radar Range Equation, Radar Detection with Noise Jamming, Beacon and Repeater Equations, MTI and Pulse Doppler Radar. Matched Filter Receiver – Impulse Response, Frequency Response Characteristic and its Derivation, Matched Filter and Correlation Function, Correlation Detection and Cross-Correlation Receiver, Efficiency of Non-Matched Filters, Matched Filter for Non-White Noise.

UNIT-II: Detection of Radar Signals in Noise: Detection Criteria – Nyman -Pearson Observer, Likelihood-Ratio Receiver, Inverse ProbabilityReceiver, Sequential Observer, Detectors– Envelope Detector, Logarithmic Detector, I/Q Detector. Automatic Detection-CFAR Receiver, Cell Averaging CFAR Receiver, CFAR Loss, CFAR Uses in Radar. Radar Signal Management– Schematics, Component Parts, Resources and Constraints.

UNIT-III: Waveform Selection: Radar Ambiguity Function and Ambiguity Diagram – Principles and Properties; Specific Cases – Ideal Case, Single Pulse of Sine Wave, Periodic Pulse Train, Single Linear FM Pulse, Noise Like Waveforms, Waveform Design Requirements, Optimum Waveforms for Detection in Clutter, Family of Radar Waveforms.

UNIT-IV: Pulse Compression in Radar Signals: Introduction, Significance, Types, Linear FM Pulse Compression – Block Diagram, Characteristics, Reduction of Time Side lobes, Stretch Techniques, Generation and Decoding of FM Waveforms – Block Schematic and Characteristics of Passive System, Digital Compression, SAW Pulse Compression.

UNIT-V: Phase Coding Techniques: Principles, Binary Phase Coding, Barker Codes, Maximal Length Sequences (MLS/LRS/PN), Block Diagram of a Phase Coded CW Radar. Poly Phase Codes : Frank Codes, Costas Codes, Non-Linear FM Pulse Compression, Doppler Tolerant PC Waveforms – Short Pulse, Linear Period Modulation (LPM/HFM), Sidelobe Reduction for Phase Coded PC Signals.

TEXT BOOKS:

1. Radar Handbook - M.I. Skolnik, 2ndEd., 1991, McGraw Hill.
2. Radar Design Principles: Signal Processing and The Environment - Fred E. Nathanson, 2nd Ed. 1999, PHI.
3. Introduction to Radar Systems - M.I. Skolnik, 3rdEd., 2001, TMH.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

REFERENCE BOOKS:

1. Radar Principles - Peyton Z. Peebles, Jr., 2004, John Wiley.
2. Radar Signal Processing and Adaptive Systems - R. Nitzberg, 1999, Artech House

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Illustrate the operation of Radar and derive the radar range equation.
2. Know the characteristics of Matched filter for non-white noise.
3. Apply the various detection criterion and types of detectors that can be used to detect the Radar signals in noise.
4. Analyse the waveform design requirements and optimum waveforms for the detection of signals in clutter.
5. Justify the significance and types of pulse compression techniques in radar signals.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	DSP PROCESSORS & ARCHITECTURES (ELECTIVE-IV)	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To learn DSP processing architectures and core algorithms used in programmable DSP.
2. To explore structural and architectural features of Texas Instruments DSP families.
3. To study VLIW architectures and the use of GPUs as alternatives for DSP applications.
4. To introduce multi-core DSP systems and parallel programming techniques using OpenMP.
5. To examine FPGA-based DSP systems and their role in advanced signal processing applications.

UNIT- I:

Programmable DSP Hardware: Processing Architectures (von Neumann, Harvard), DSP core algorithms (FIR, IIR, Convolution, Correlation, FFT), IEEE standard for Fixed- and Floating-Point Computations, Special Architectures Modules used in Digital Signal Processors (like MAC unit, Barrel shifters), On-Chip peripherals, DSP benchmarking.

UNIT- II:

Structural and Architectural Considerations: Parallelism in DSP processing, Texas Instruments TMS320 Digital Signal Processor Families, Fixed Point TI DSP Processors: TMS320C1X and TMS320C2X Family, TMS320C25 –Internal Architecture, Arithmetic and Logic Unit, Auxiliary Registers, Addressing Modes (Immediate, Direct and Indirect, Bit-reverse Addressing), Basics of TMS320C54x and C55x Families in respect of Architecture improvements and new applications fields, TMS320C5416 DSP Architecture, Memory Map, Interrupt System, Peripheral Devices, Illustrative Examples for assembly coding.

UNIT- III:

VLIW Architecture: Current DSP Architectures, GPUs as an alternative to DSP Processors, TMS320C6X Family, Addressing Modes, Replacement of MAC unit by ILP, Detailed study of ISA, Assembly Language Programming, Code Composer Studio, Mixed and Assembly Language programming, On-chip peripherals, Simple applications developments as an embedded environment.

UNIT- IV:

Multi-core DSPs: Introduction to Multi-core computing and applicability for DSP hardware, Concept of threads, introduction to P-thread, mutex and similar concepts, heterogeneous and homogenous multi-core systems, Shared Memory parallel programming –OpenMP approach of parallel programming, PRAGMA directives, OpenMP Constructs for work sharing like for loop, sections, TI TMS320C6678 (Eight Core subsystem)

UNIT-V:

FPGA based DSP Systems: Limitations of P-DSPs, Requirements of Signal processing for Cognitive Radio (SDR), FPGA based signal processing design-case study of a completedesign of DSP processor.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	WIRELESS COMMUNICATIONS LAB	L	T	P	C
		0	1	2	2

COURSE OBJECTIVES:

1. To provide hands-on experience with Software Defined Radio (SDR) platforms for signal generation and detection.
2. To learn and implement digital modulation techniques such as BPSK, QPSK, QAM, and FSK.
3. To explore virtual lab tools like LabVIEW and MATLAB for simulation and analysis of wireless communication systems.
4. To design and evaluate baseband modulation schemes under various channel conditions.
5. To implement synchronization, channel estimation, and equalization techniques for OFDM-based systems.

PART A: List of Experiments :(Minimum of Ten Experiments has to be performed)

1. Measurement of Bit Error Rate using Binary Data
2. Verification of minimum distance in Hamming code
3. Determination of output of Convolution Encoder for a given sequence
4. Determination of output of Convolution Decoder for a given sequence
5. Efficiency of DS Spread- Spectrum Technique
6. Simulation of Frequency Hopping (FH) system
7. Effect of Sampling and Quantization of Digital Image
8. Verification of Various Transforms (FT / DCT/ Walsh / Hadamard) on a given Image (Finding Transform and Inverse Transform)
9. Point, Line and Edge detection techniques using derivative operators.
10. Implementation of FIR filter using DSP Trainer Kit (C-Code/ Assembly code)
11. Implementation of IIR filter using DSP Trainer Kit (C-Code/ Assembly code)
12. Determination of Losses in Optical Fiber
13. Observing the Waveforms at various test points of a mobile phone using Mobile Phone Trainer
14. Study of Direct Sequence Spread Spectrum Modulation & Demodulation using CDMA-DSSBER Trainer
15. Study of ISDN Training System with Protocol Analyzer
16. Characteristics of LASER Diode.

PART B: Equipment required for Laboratory Software:

1. MATLAB along with Simulink Licensed simulation software tool with communication and Signal processing Toolbox.
2. Computer Systems with required specifications.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

Hardware:

1. Hardware kits for verification of BER
2. Hardware kits of Convolution encoders, Hamming encoders.
3. Frequency spectrum
4. Mobile Phone Trainer
5. DSP Trainer Kit
6. CDMA-DSS-BER Trainer
7. ISDN Training System with Protocol Analyzer
8. Optical fiber Transmitter and receiver kit along with different lengths of cables

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Perform signal sampling, reconstruction, and multiplexing using SDR hardware platforms.
2. Generate and detect various digital modulation schemes including BPSK, QPSK, QAM, and FSK.
3. Use LabVIEW and MATLAB to simulate baseband modulation and analyse performance under AWGN and multipath channels.
4. Implement synchronization techniques such as STO and CFO estimation for QAM/QPSK and OFDM systems.
5. Apply channel equalization methods and preamble-based synchronization for robust wireless communication system design.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	SYSTEM DESIGN WITH RTOS & EMBEDDED LINUX LAB	L	T	P	C
		0	1	2	2

COURSE OBJECTIVES:

1. To provide practical exposure to task management and scheduling in real-time operating systems.
2. To learn synchronization mechanisms such as semaphores and mutexes in multitasking environments.
3. To explore interrupt handling, inter-process communication, and memory management techniques.
4. To develop programs that simulate real-time OS behaviour and resource management.

List of Experiments:

1. Develop a program that involves various operations related to real-time tasks, such as creation, deletion, and synchronization.
2. Implement a program to understand and simulate task scheduling algorithms in a real-time operating system.
3. Create a program to explore the concept and mechanisms of context switching in the context of real-time operating systems.
4. Develop a program to illustrate the use of semaphores for synchronization and communication between different tasks.
5. Implement a program that demonstrates the usage of mutexes for managing critical sections in a multitasking environment.
6. Write a program to handle interrupts effectively, exploring the ways an operating system manages and responds to external interrupts.
7. Develop a program to manage and manipulate queues within the real-time operating system environment.
8. Implement a program that showcases the effective management of pipes for inter-process communication.
9. Write a program to understand and implement signal handling mechanisms within the real-time operating system.
10. Develop a program that explores memory management techniques in the context of real-time operating systems.

COURSE OUTCOMES:

After completion of the course, students will be able to:

1. Implement and manage real-time tasks including creation, deletion, and synchronization.
2. Simulate and analyze task scheduling algorithms and context switching mechanisms.
3. Apply synchronization tools like semaphores and mutexes to manage concurrent tasks.
4. Handle interrupts and design inter-process communication using queues, pipes, and signals.
5. Demonstrate memory management strategies suitable for real-time operating systems.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

II Semester	SEMINAR-II	L	T	P	C
		0	0	2	1



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

III Semester	RESEARCH METHODOLOGY AND IPR	L	T	P	C
		3	0	0	3

COURSE OBJECTIVES:

1. To learn the concept of a research problem and methods for its investigation.
2. To develop skills in literature review, plagiarism detection, and research ethics.
3. To learn techniques for technical writing and research proposal preparation.
4. To gain foundational knowledge of Intellectual Property Rights (IPR) and patenting processes.
5. To explore new developments in IPR, including applications in software, biology, and traditional knowledge.

UNIT-I: Meaning of research problem, Sources of research problem, Criteria Characteristics of a good research problem, Errors in selecting a research problem, Scope and objectives of research problem. Approaches of investigation of solutions for research problem, data collection, analysis, interpretation, Necessary instrumentations

UNIT-II: Effective literature studies approaches, analysis Plagiarism, Research ethics, Effective technical writing, how to write report, Paper Developing a Research Proposal, Format of research proposal, a presentation and assessment by a review committee.

UNIT-III: Nature of Intellectual Property: Patents, Designs, Trademarks and Copyright. Process of Patenting and Development: technological research, innovation, patenting, development. International Scenario: International cooperation on Intellectual Property. Procedure for grants of patents, Patenting under PCT.

UNIT-IV: Patent Rights: Scope of Patent Rights, Licensing and transfer of technology, Patent information and databases, Geographical Indications.

UNIT-V:

New Developments in IPR: Administration of Patent System. New developments in IPR, IPR of Biological Systems, Computer Software etc. Traditional knowledge Case Studies, IPR and IITs.

TEXT BOOKS:

1. Stuart Melville and Wayne Goddard, “Research methodology: an introduction for science & engineering students”.
2. Wayne Goddard and Stuart Melville, “Research Methodology: An Introduction”

REFERENCES:

1. Ranjit Kumar, 2nd Editio , “Research Methodology: A Step by Step Guide for beginners”
2. Halbert, “Resisting Intellectual Property”, Taylor & Francis Ltd, 2007.
3. Mayall, “Industrial Design”, McGraw Hill, 1992.
4. Niebel , “Product Design”, McGraw Hill, 1974.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

5. Asimov, “Introduction to Design”, Prentice Hall,1962.
6. Robert P. Merges, Peter S. Menell, Mark A. Lemley, “Intellectual Property in New Technological Age”,2016.
- 7.T. Ramappa, “Intellectual Property Rights Under WTO”, S. Chand,2008

COURSE OUTCOMES:

At the end of this course, students will be able to:

1. Learn research problem formulation.
2. Analyze research related information
3. Follow research ethics
4. Learn that today’s world is controlled by Computer, Information Technology, buttomorrow world will be ruled by ideas, concept, and creativity.
5. Understanding that when IPR would take such important place in growth of individuals & nation, it is needless to emphasis the need of information about Intellectual Property Right to be promoted among students in general & engineering in particular.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

III Semester	SUMMER INTERNSHIP/INDUSTRIAL TRAINING	L	T	P	C
		0	0	0	3



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

III Semester	COMPREHENSIVE VIVA#	L	T	P	C
		0	0	0	2



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

III Semester	DISSERTATION PART- A^S	L	T	P	C
		0	0	20	10



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
R25 M.Tech ELECTRONICS & COMMUNICATION ENGINEERING
SIGNAL PROCESSING AND COMMUNICATIONS

IV Semester	DISSERTATION PART– B%	L	T	P	C
		0	0	32	16